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7590 02/03/2004

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LOREN DONALD PEARSON	(Depositor's name)
<i>Loren D. Pearson</i>	REG. NO. 42,987 (Signature)
May 3, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/619,970	07/15/2003	Till Schlosser	P2002,0599	7834

TITLE OF INVENTION: INTEGRATED SEMICONDUCTOR MEMORY AND FABRICATION METHOD

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	05/03/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LEE, CALVIN	2825	438-244000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Laurence A. Greenberg
 2 Werner H. Stemer
 3 Ralph E. Locher

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Infineon Technologies AG

Muenchen, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) *Loren D. Pearson* (Date) 5/3/04

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01 FC:1501
 02 FC:1504

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